

In Place of FORM PTO-1449 (Modified)

Serial No.:

Applicant:

Michael Eneboe, et al.

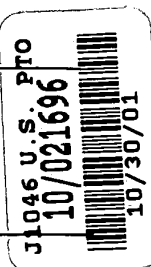
Filing Date:

October 30, 2001

Group:

Atty. Docket No.: 01-490

**LIST OF PATENTS AND PUBLICATIONS FOR  
APPLICANT'S INFORMATION DISCLOSURE  
STATEMENT**



Reference Designation

**U.S. PATENT DOCUMENTS**

Examiner Initial	Document Number	Date	Name	Class	Subclass	Filing Date if Appropriate
<input checked="" type="checkbox"/> AAA	6,152,613	11/28/00	Martin et al.	395	800.33	11/25/97
<input checked="" type="checkbox"/> ABA	5,898,677	04/27/99	Deeley et al.	370	276	01/13/97
<input checked="" type="checkbox"/> ACA	5,752,070	05/12/98	Martin et al.	395	800.33	07/08/94
<input type="checkbox"/> ADA						
<input type="checkbox"/> AEA						
<input type="checkbox"/> AFA						
<input type="checkbox"/> AGA						
<input type="checkbox"/> AHA						
<input type="checkbox"/> AIA						
<input type="checkbox"/> AJA						

**FOREIGN PATENT DOCUMENTS**

Examiner Initial	Document Number	Date	Country	Class	Subclass	Translation Yes No
<input type="checkbox"/> AKA						
<input type="checkbox"/> ALA						
<input type="checkbox"/> AMA						
<input type="checkbox"/> ANA						
<input type="checkbox"/> AOA						

**OTHER ART (Including Author, Title, Date, Pertinent Pages, Etc.)**

Examiner

Initial

☒ APA

"On-chip Networks weighed as wiring alternative," by Ron Wilson, Integrated System Design, June 25, 2001;  
<http://www.eet.com/story/OEG20010625S0099>, July 3, 2001.

☒ AQA

"Configurable Computing," by John Villaseñor and William H. Mangione-Smith; Scientific American, June 1997,  
<http://www.sciam.com/0697issue/0697villasenor.html>, August 7, 2001.

☐ ARA

Examiner:

VUTHE STEK

Date Considered:

6/4/2003

EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.

# 3

In Place of FORM PTO-1449 (Modified)

Serial No.: 10/021,696

Applicant: Michael Eneboe, et al.

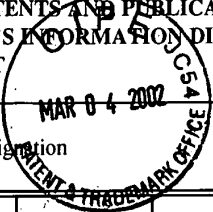
Filing Date: October 30, 2001

Group:

Atty. Docket No.: 01-490

LIST OF PATENTS AND PUBLICATIONS FOR  
APPLICANT'S INFORMATION DISCLOSURE  
STATEMENT

MAR 04 2002


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Reference Designation

U.S. PATENT DOCUMENTS

Examiner Initial	Document Number	Date	Name	Class	Subclass	Filing Date if Appropriate
___ AAA						
___ ABA						
___ ACA						
___ ADA						
___ AEA						
___ AFA						
___ AGA						
___ AHA						
___ AIA						
___ AJA						

FOREIGN PATENT DOCUMENTS

Examiner Initial	Document Number	Date	Country	Class	Subclass	Translation Yes No
___ AKA						
___ ALA						
___ AMA						
___ ANA						
___ AOA						

OTHER ART (Including Author, Title, Date, Pertinent Pages, Etc.)

Examiner

Initial

1/5

APA "Self-Reconfigurable Programmable Logic Device", Reetinder Sidhu, et al., University of Southern California, Office of Technology Licensing, Los Angeles, CA, September 7, 2001.

\_\_\_ AQA

\_\_\_ ARA

Examiner:

VUTHE SIER

Date Considered:

6/4/2003

EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.